**HW4 CS375**

**2.7** translate 0xabcdef12 to decimal

1010101111001101111011111100 binary

2882400018 decimal

**2.9**

addi x9, x6, #8       x9 = A[8]

add x10, x6, XZR   x10 = A[0]

STUR x10 [x9, #0] A[0] = A[8]

LDUR x9 [x9, #0]  A[8] = A[8]

add x0, x9, x10     f = A[8] + A[8]

**2.16** Provide instruction type, assembly language instruction, and binary representation of instruction described by the following LEGv8 code

op=0x7c2, Rn=12, Rt=3, const=0x4

D type instruction: 7c2 4 0 12 3

Assembly instruction: LDUR x3, [x12, #4]

Binary representation: 11111000010 000000100 00 01100 00011